

# Locate Missing Timing Arcs

## To Prevent Chip Design Failures

- ◆ For each pin in .Lib model, perform the tasks below
  - Find all possible timing arcs from cell's function
  - Locate the existed timing arcs with corresponding 'when' conditions in .Lib model
  - Report the missing timing arcs not in .Lib model
- ◆ Those missing timing arcs could be accessed in the designs generated from synthesis tools.
- ◆ Misleading info on missing timing arcs could cause chip design failures and very difficult to debug.