

MSIM[®] RC Reduction

Layout-Extracted Circuit Simulation

- ◆ Benchmark on D-Type Flip Flop circuit

MOSFET: 18 Resistor & Capacitor: 1,000+

	Standard*	MSIM [™]	Comparison
CPU Time	300 sec	10 sec	30 X

The inaccuracy is less than 1%.

- ◆ Benchmark on SRAM 'RR256x16'

MOSFET: 39,464 Resistor : 200,526 Capacitor: 316,934

	Standard*	MSIM [™]	Comparison
Accuracy	3.41 ns	3.39 ns	0.59 %
CPU Time	3,867 sec	317 sec	12.2 X

* Standard means the most popular Spice simulator

Technology Leader in IP Characterization and IC/PCB Simulation

Legend
Design Technology