

フラットパネルディスプレイ設計検証フロー

Flat Panel Display Design Solution

Circuit Design

回路図エディタ・シミュレーション解析
C³ (Circuit-Cube)

Circuit Simulator

MSIM-LCD

HSI
Spice

MSIM-LCD

CoSimLCD

Pixel Design

3D液晶シミュレータ
ExpertLCD

電磁光学シミュレータ
ExpertOLED

Panel Layout Design

レイアウトエディタ
Ismo
FineArts
FineSketch

カラーフィルタ設計
FineCFC
FPC設計
FineFPC

Panel Layout Verification

寄生抵抗・容量抽出 電圧降下検証
FineAcres | FineQap | FineResQ | FineVolt

検証(DRC, LVS, LVL, ERC)
IDRC/LVS | FineLVL | FineERC
HayatoDRC

Reticle Design

ガラスフロアプラン
FineGFP

レチクルフロアプラン
FineRFP

バックアノテーション

α-SXTM-タペース
OpenAccessTM-タペース



MSIM[®]-LCD

Simulation, Model Extraction & Refining

- ◆ Integrated in Jedat's Flat-Panel Display Design & Verification Flow.
- ◆ Support TFT, OTFT, OLED and IGZO models.
- ◆ Provide a complete solution for simulation, and model extraction & refining.
- ◆ Offer the services of model extraction & refining.
- ◆ Deliver best price-performance with excellent values.

MSIM[®] Certifications

- ◆ MSIM certified by TSMC's Spice Tool Qualification Program

<http://www.legenddesign.com/BW/021009.shtml>

- ◆ JEITA (Japan Electric and Information Technology Industries Association) has qualified MSIM-PCB for simulating IBIS models in PCB designs. Those results can be downloaded from JEITA web:

<https://ec.jeita.or.jp/ibis/>