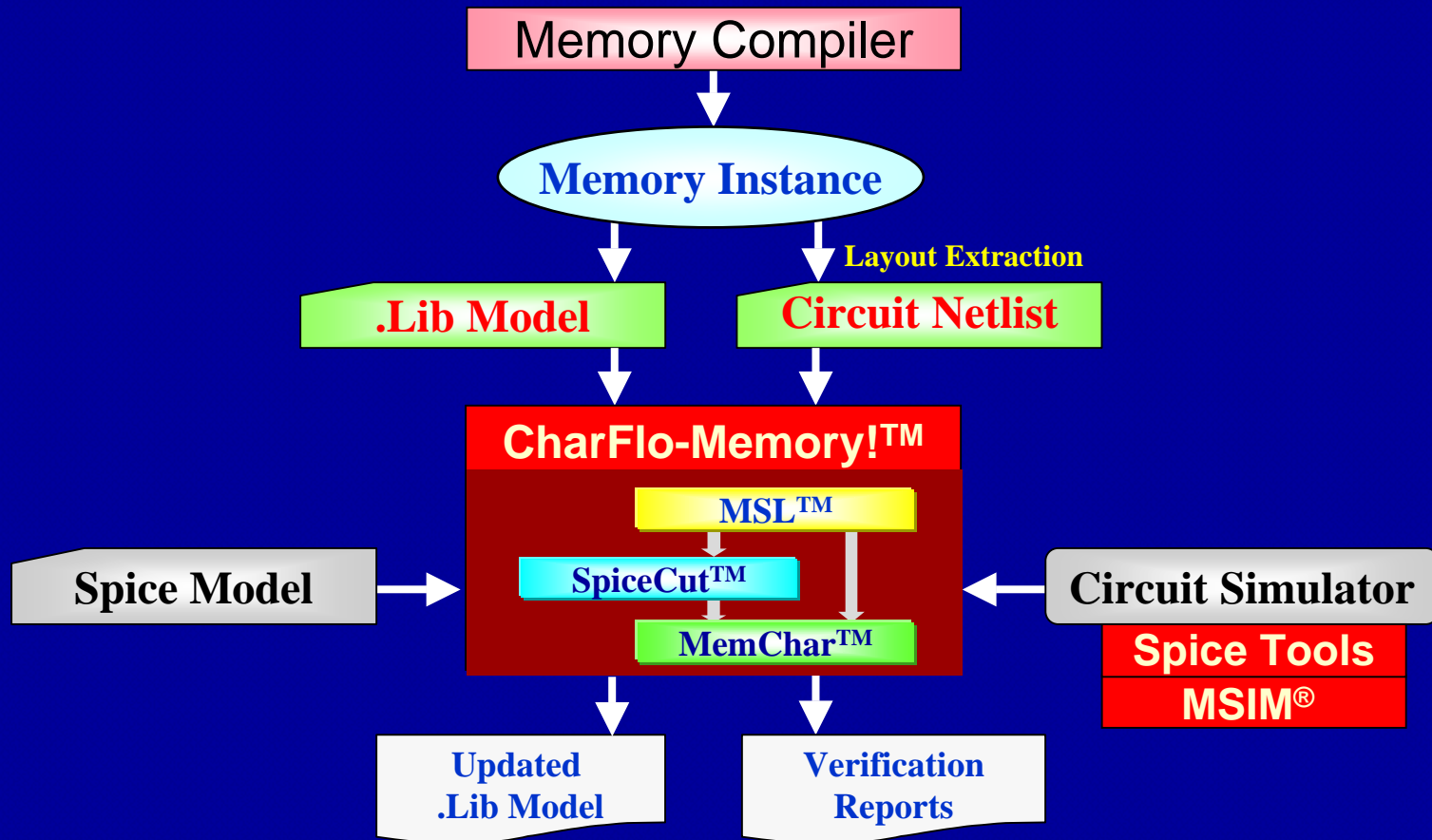


Characterization Flow For Compiled Memory Instances



The Features

Legend's CharFlo-Memory! Tool Set

- ◆ Production & silicon proven over numerous designs
- ◆ Perform “on-the-fly” verification for high reliability
- ◆ Build critical-path circuits from layout-extracted circuit for high performance
- ◆ Enable ‘active-net’ only layout extraction, RC reduction, and SPDM function for high productivity
- ◆ Support CCS timing, power and noise models
- ◆ Foundry and IP vendors’ support
- ◆ Excellent price-performance and automation